IMPORTANT PRODUCT INFORMATION

GFK-2068BK Oct 2022

VersaMax Micro PLC Backup Battery

IMPORTANT PRODUCT INFORMATION



Warnings and Caution Notes as Used in this Publication

A WARNING

Warning notices are used in this publication to emphasize that hazardous voltages, currents, temperatures, or other conditions that could cause personal injury exist in this equipment or may be associated with its use.

In situations where inattention could cause either personal injury or damage to equipment, a Warning notice is used.

A CAUTION

Caution notices are used where equipment might be damaged if care is not taken.

Note: Notes merely call attention to information that is especially significant to understanding and operating the equipment.

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Introduction

This document contains important information about the following VersaMax* Nano Programmable Logic Controller (PLC) and Micro PLC models:

Part Numbers			
IC200NAL110-EG	IC200UDD212-EK	IC200UAR014-EL	IC200UDR064-EK
IC200NAL211-EG	IC200UDD220-DF	IC200UAR028-EK	IC200UDR120-DF
IC200NDD010-GJ	IC200UDD240-EF	IC200UDD020-DF	IC200UDR140-EF
IC200NDD101-GJ	IC200UDR001-EL	IC200UDD040-EF	IC200UDR164-EK
IC200NDR001-GJ	IC200UDR002-EL	IC200UDD064-EK	IC200UDR228-DD
IC200NDR010-GJ	IC200UDR003-EL	IC200UDD104-EL	IC200UDR440-EF
IC200UAA003-EL	IC200UDR005-EK	IC200UDD110-EK	IC200UMB001-C
IC200UAA007-EK	IC200UDR006-EK	IC200UDD112-EL	IC200USB001-C
IC200UAL004-EK	IC200UDR010-EK	IC200UDD120-EK	IC200USB002-C
IC200UAL005-EK	IC200UDR020-DF	IC200UDD164-EK	IC200UUB001-C
IC200UAL006-FL	IC200UDR040-EF		

The suffix letters following Central Processing Unit (CPU) part numbers indicate the hardware version followed by the firmware version. Expansion units do not have firmware, so only the hardware version is indicated.

This firmware release resolves an issue with Series Ninety Protocol (SNP) and adds enhancements to the Modbus Real-Time Update (RTU) protocol.

Functional Compatibility

- **Programming software:** Release 4.01 features require PAC Machine Edition version 6.0 SIM 1 or later
- Micro 20/40/64 CPU Firmware Compatibility: Release 4.01 requires CPUs with 512 KB of SRAM (new CPU revision levels listed above). This release is not compatible with older CPU versions (with 256 KB SRAM).
- Release 3.84 is compatible with the old and new revisions of CPU hardware.
- Minimum Hardware Versions for Micro-20, -40, -64 CPU Compatibility with Firmware Version 4.00:
- These 20/40/64pt models with enhanced SRAM are referred to as *Micro PLUS* controllers to differentiate from earlier Micro 20/40/64pt models.
- <u>IMPORTANT</u>: Releases 4.00 and later require the CPUs with enhanced memory (Micro PLUS CPUs). This release is not compatible with the older versions of CPUs. But firmware of earlier releases (i.e. before 4.0) is compatible with the new revision of CPU hardware.

Part Numbers		
IC200UDD020-Bx	IC200UDR140-Bx	
IC200UDD220-Bx	IC200UDR440-Bx	
IC200UDR020-Bx	IC200UDD064-Bx	
IC200UDR120-Bx	IC200UDD164-Bx	
IC200UDD040-Bx	IC200UDR064-Bx	
IC200UDD240-Bx	IC200UDR164-Bx	
IC200UDR040-Bx		

- Config equality with Machine Edition (6.0 SIM1 and above) and Old Firmware (before Rel 3.83): After auto-configuration, upon upload of configuration from PLC with older firmware versions, the status bar of the Machine Edition window shows Config NE. This issue applies to all VMNM CPUs.
- Note that this issue is not present with the new (Rel 3.83 and above) firmware.
- VersaMax Micro 64 requires Machine Edition 5.5 SP2 SIM5 or later.
- Support for Micro Motion Modules in the VersaMax Micro PLC system requires Machine Edition 5.7 SIM 3 or later.
- The motion functionality available on VersaMax Micro CPUs is not compatible with VersaMotion servo amplifiers IC800VMA012, 022, 042, 072, 102, 202, or 302.
- Performance: Review the *VersaMax Nano/Micro PLC User's Manual*, GFK-1645J, or later for updated performance information.
- Micro-40 CPU and Ethernet Option Module: Micro-40 CPUs are compatible with Ethernet Option Module IC200UEM001-AB and later revisions only.
- Firmware version 3.59 is compatible with all previous hardware revisions of the Micro 14, Nano, Analog Nano, Micro 28, Micro 28, and Micro 28 ESCP CPUs.
- VersaPro 2.04/Machine Edition 5.0 and later releases can be used with firmware release 3.59 (Micro 14, Nano, Analog Nano, Micro 23, Micro 28, and Micro 28 ESCP CPUs).
- Micro 14, Nano, Analog Nano, CPU Hardware Compatibility:
 The CPU hardware versions listed below require version 3.56 firmware or later. They are not compatible with any prior firmware release.

Part Numbers	
IC200NAL110-Cx	IC200UAR014-Cx
IC200NAL211-Cx	IC200UDD104-Cx
IC200NDD010-Dx	IC200UDD112-Cx
IC200NDD101-Dx	IC200UDR001-Cx
IC200NDR001-Dx	IC200UDR002-Cx
IC200NDR010-Dx	IC200UDR003-Cx
IC200UAA003-Cx	

- All VersaMax Micro CPUs support 8- and 16-point expansion units even though older programming packages may not have them in the configuration. To configure these expansion units with VersaPro or Machine Edition, allow the CPU to autoconfigure the expansion units, and upload the configuration into the software tool.
- Battery: Backup battery options are available for 20/23/28/40/64-point VersaMax Micro PLCs to preserve the RAM contents and the CPU's real-time clock. For available types of backup batteries, see "Error! Reference source not found." on page 5.

Firmware Upgrade Kits

Available at https://www.emerson.com/Industrial-Automation-Controls/support.

Part Number	Description
82A1605-MS10-000-A3	R4.03 for Micro PLUS-64 CPUs with 512K RAM
82A1604-MS10-000-A3	R4.03 for Micro PLUS-40 CPUs with 512K RAM
82A1603-MS10-000-A3	R4.03 for Micro PLUS-20 CPUs with 512K RAM
44A752236-G10	R3.71 for Micro-14 CPUs
44A752239-G07	R3.71 for Nano CPUs except IC200NAL110/211
44A752240-G07	R3.71 for Analog Nano CPUs IC200NAL110/211
44A752237-G10	R3.71 for Micro-23, and -28 CPUs except IC200UDD120
44A752238-G10	R3.71 for Micro-28 CPU with ESCP, IC200UDD120
44A753290-G05	R3.84 upgrade kit for Micro 20pt Models with 256K RAM
44A753291-G05	R3.84 upgrade kit for Micro 40pt Models with 256K RAM
44A753036-G10	R3.84 upgrade kit for Micro 64pt Models with 256K RAM

Protocol Upgrade Kits (Ethernet Drivers)

Available at https://www.emerson.com/Industrial-Automation-Controls/support.

Upgrade Kit Number	Description
82A1587-MS10-000-B0	SRTP upgrade kit
82A1588-MS10-000-B0	Modbus TCP upgrade kit
82A1589-MS10-000-B0	Pass through upgrade kit

Electronic Documentation Available

VersaMax Nano/Micro PLC user documentation, specifications, and CAD drawings are available for download from the Technical Support website, https://www.emerson.com/Industrial-Automation-Controls/support.

Problems Resolved in Release 4.03

With VMNM CPU 40 point & 64 points, while all inputs are kept ON, then a few of these inputs may fluctuate momentarily.

Restrictions/Operating Notes

Issue	Description
Selection of Communication Protocols in VersaMax Nano/Micro PLCs	When devices such as Operator Interface/HMI communicate with VersaMax Nano Micro, the use of Modbus Protocol is recommended for better performance.
Piggyback Status using SNP Master	An SNP Master Protocol Long Attach COMMREQ may return all zeros in the piggyback status word for the slave device when using a modem or when used at 38400 bps.
Data Transfer Between PLCs using a SET (Serial Ethernet) Module	CPU with a SET module connected to Port 2, when configured for Serial IO with a data rate of 38400, does not allow the slave PLC to read the data written by the master PLC, that is, the values returned are all zeros. The data is read correctly when an Ethernet option board is used instead of the SET module.
CPU becomes nonfunctional when HSC is configured for minimum count span and subjected to the higher input frequency.	When HSC (16-bit/32-bit) counter is configured for a minimum count span of 0, 1, or 2 at the maximum frequency, 100 kHz, the Watchdog timer expires, the OK LED is switched OFF, and only the Power LED glows.
Under specific high and low limit ranges, the HSC one-shot mode is not working as per the information provided in the user manual.	For HSC one-shot mode, the user manual states that for up counting the counter stops at N+1, and for down counting the counter stops at N-1. The expected behavior is observed when both high and low limits are configured with negative values, or a high limit is configured with a positive value and a low limit is configured with a negative value. But if both high and low limits are configured with positive values, when down counting, the counter stops at N instead of N-1. When up counting with this configuration, the counter stops at N+1 as expected. Example: If the low limit is 10 and the high limit is 100, for up counting it stops at 101 (i.e. N+1) but for the down count it stops at 10 (i.e. N) instead of 9 (i.e. N-1).
At higher input frequencies, the HSC Type-B 32-bit counter doesn't count input pulses properly.	At higher input frequencies, say 100 kHz, the number of "A quad B" phase inputs given does not match the counts counted by the Type B 32-bit counter.

Issue	Description
Modbus/TCP Client	
feature stops working while the CPU is fully loaded, or Modbus/TCP Client (Rx3i) gives error 0x0B91 for one of seven TCP server channels opened in the VersaMax Nano/Micro PLC Watchdog times out when SNPX write command is triggered	. Under full load conditions (Example: Port 1 and Port 2 engaged in Modbus RTU communication, PTO at 65KHz, 3 HSC channels configured as counters, and 4 expansion modules connected), sometimes the Micro does not respond to one of the Modbus commands. Under such full load conditions, Micro's UART occasionally fails to process one-byte information, which is the cause of the problem. This issue is not observed with a 175 Kbps rate. To correct this problem, have the Client PLC resend the command when the error 0x0B91 is received. Whenever an SNPX write command is fired after updating the real-time datagram command, the watchdog times out for the few combinations of SNP timer values
after the Update real- time datagram command.	Example: T1 = 100, T2 = 0 (Disabled), T3 = 0 (Disabled), T4 = 2000, T5 = 0 (Disabled).
Winloader can update the firmware with OEM locked.	When the OEM key is enabled, updating the firmware should be possible only by entering the OEM key password. Currently, even when the OEM key is enabled, a firmware update is possible without a password prompt.
Uploading the logic with real data results in losing the precision of the real constant values.	When user logic containing the REAL data values is uploaded, its precision is lost in Machine Edition and hence Equality of the logic is lost. This is because Machine Edition represents the value of a real number in scientific notation with the precision of 6 digits after the decimal point. For example, if a value of 2147483648.0 is uploaded, it is represented in Machine Edition as 2.147484e+009 (i.e. only 6 digits after decimal), which is equivalent to 2147484000.0 (round off, which is an error). If this value is downloaded to the CPU, it is stored as 2147483904.0 (rounded off, which is expected as per the IEE-754 32-bit representation of the REAL type).
Changing retentive property through RMS may cause an error	When the retentive property of a variable is changed and a Run Mode Store (RMS) is subsequently performed, Machine Edition's controller status display is updated to Logic EQ. Also, the Feedback zone shows that a particular program block is downloaded. However, the Feedback zone displays the following Warning: "Warning 8545-Project and Controller logic are equal. Run mode store will not be performed." Similar behavior sometimes occurs while changing the data type of a reference.
Verify Equality does not work properly after attempting an invalid word-for-word change.	If an attempt to make a word-for-word change results in Logic Not Equal, then undoing the change or reverting to the original logic does not result in Logic Equal. To make the Logic in Machine Edition and CPU equal again, the logic needs to be downloaded to the CPU.

Issue	Description
13300	. This issue is present when Port 2 is configured for Ethernet with
Under a few conditions, changing the Ethernet Board configuration causes the CPU to go to Blink mode	Modbus Client Enabled and under the following conditions: Machine Edition hardware configuration is downloaded to the Micro, with Ethernet Option Module configuration changed, particularly from 175K to 38.4K baud with also change in parity. Subsequently saving the Ethernet Module configuration causes the Stop Faulted mode of Micro, with its OK LED blinking. To recover from this faulted mode, cycle power to the Micro PLC. (Micro 20/40/64)
In Machine Edition, uploading a configuration and then changing the Port 2 Configuration in CPU Settings can result in non-default values being displayed on the Port 2 settings tab.	To work around this issue, change the Port Mode setting of Port 2 to a different protocol and then back again. (For example, change Port Mode from SNP to RTU and back to SNP again.) After this procedure, the Port 2 tab will show default settings. (Micro 20/40/64)
Micro PLC port fails to restore to RTU protocol; port stays in SNP Mode.	When a Micro port configured as RTU Slave (in communication with RTU Master) is connected to the programmer, it switches port protocol to SNP and communicates to the programmer. After disconnecting the programmer, the port will switch back to the RTU protocol after 12 seconds. If any data is sent to Micro during this 12-second delay the micro port will be locked in the SNP mode and a power cycle is required to resume the operation. (Micro 20/40/64)
Analog Expansion Module Failures	When an analog expansion module (IC200UEX616, IC200UEX626, IC200UEX636) fails, it drives a signal low, which in turn causes the loss of all other analog expansion units in the system. Discrete expansion units cabled after the first analog expansion unit will also be lost. This failure can also subsequently affect the operation of any discrete expansion units, should any of them later be powered down. Consult the user's manual, GFK-1645, for detailed troubleshooting instructions to be used in isolating expansion unit failures. (Micro 20/40/64)
DIP Switch Controls Analog Scaling Range	DIP Switch SW6 on an analog expansion unit controls the maximum value for the unit's analog inputs and analog outputs as follows: SW6: OFF \Rightarrow 4096 (32767 counts = 10V) ON \Rightarrow 4000 (32000 counts = 10V, 32767 counts = 10.23V) By default, DIP Switch SW6 should be set in the OFF position (32726 counts = 10V). (Micro 14/23/28/28 ESCP/20/40/64)
PWR LED Status on Analog Expansion Units	On the analog expansion units (IC200UEX616, IC200UEX626, IC200UEX636), the PWR LED will be on if any other expansion unit or the CPU to which it is connected is powered up. This is due to a "power bleed" issue in the cabling to the unit. (Micro 20/40/64)

Issue	Description
	For all Nano and Micro PLCs that support High-speed Counters, input
Input References Used by the High-Speed Counter	references %I489 - %I493 are reserved for the embedded HSC function
	and are not available to the application program.
	Recommendation : Avoid using references %I489 - %I493 (which are
	located immediately before the rest of the HSC input references) in the
	application program.
	For all Nano and Micro PLC types that support High-speed Counters,
	when sending data commands to the High-speed Counter using the
	COMMREQ function, the CPU ignores the data type and start location
	parameters. These parameters specify where the command and data
High-speed Counter	words are located in CPU memory. The CPU expects these three
COMMREQs:	parameters to be located immediately after the command block.
COMMINEQS.	Recommendation: Be sure the command word and data words are
	located contiguously in memory, with the rest of the COMMREQ
	command block. They should immediately follow the "Start Location of
	Command Word" in the COMMREQ command block.
	For all Nano and Micro PLC CPUs, hardware flow control should only be
	configured if the Micro PLC will be used with a modem or other device
Hardware Flow Control	that requires that functionality. In a point-to-point setup, configuring the
Hardware Flow Control	, , , , , , , , , , , , , , , , , , , ,
	units for hardware flow control may cause the units to fail to respond.
	The default setting is no flow control.
	Micro-23, -28, and -64 PLC CPUs, RTU communications on port 2 are not
	reliable with large programs when scan times are above 140
DTU 6	milliseconds. Using Constant Sweep mode may improve communications
RTU Communications	by allowing a larger communications window at the end of the sweep.
	When uploading a configuration from the CPU to the Programmer, 8 and
	16-point expansion units may show up in the configuration as other
	expansion modules, but with similar I/O sizes.
Serial Option Module	Option Modules IC200USB001 and USB002 for Micro-20/40/64 PLCs do
Signal Pins:	not contain RTS/CTS signal lines. These ports cannot be used with
	modems and repeaters that require hardware flow control.
	The Memory Option module IC200UMB001 for Micro-20/40/64 CPUs has
Memory Option	a Write-Protect switch. The default for this switch is to allow all write
Module Write-Protect	operations. If the switch is changed to prevent writing to the memory
Switch:	module, there is no feedback to the CPU that the write has not occurred.
	Use caution when changing this switch position.
	When using the USB Option module with a 20/40/64 Point CPU, the
Communicating through USB:	Machine Edition configuration parameters for Port 2 should be left at
	their defaults: 19200, Odd parity, 1 stop bit, and no flow control. These
	parameters match the FTDI USB driver configuration in Windows.
an oagn obb.	Do not unplug the USB cable while communicating with the unit.
	Windows requires that the device be stopped through the USB driver
	before disconnecting the cable.
Logic-Driven Write to	Parameters stored to a Micro-14/23/28/20/40/64 CPU using the Logic
Flash	Driven write to Flash feature are erased during a firmware upgrade

Issue	Description
	For all Nano and Micro PLC types that support High-speed Counters,
Counter Disabled Functionality for PTO:	when a channel is being configured for PTO or PTO w/Ramp, if the
	Counter Disabled parameter is visible in the programming software, it
	has no effect. Counter Disabled is only used for channels configured as
	Type A or B High-speed Counter.
	For all Nano and Micro PLC CPUs, care must be taken to set timeouts and
	message lengths when operating at 4800 bps or lower. Messages at this
Communication Timing at 4800 bps and Lower	rate are longer in duration and may cause various timeouts within a
	system. This can become worse on the Micro-64 CPU because the scan
	times on this unit are typically slightly longer.
	When opening multiple monitoring windows in Machine Edition or
	VersaPro while <i>connected</i> to a CPU, one of the following error messages
	may be observed: "0x010D: Lost communication with COM server" or
Monitoring Using	Monitoring Error: "Too many windows open." If this happens, close the
Machine Edition	error window; exit the Machine Edition application, and then reopen
	Machine Edition to resume monitoring. Disconnecting and reconnecting
	Machine Edition may solve the problem.
	For all Nano and Micro PLC types that support High-speed Counters, the
	timebase register does not provide consistent results when the HSC is
High-Speed Counter	used to measure signals of continuously varying frequency (the data
Signal Frequency	values are not consistent). This does not happen while measuring a
	signal with constant frequency.
Shorting Signals on	On Micro-23, -28, and -64 PLC CPUs, a short on the 5V line on Port 2
Port 2 Causes Reset	causes the CPU to power off. Power returns when the short is removed.
F.1	If four SRTP channels are active at the same time, communication does
Ethernet Option	not take place on COM1, so the CPU cannot communicate with the
Module on Micro-	Machine Edition programmer on COM1. Communications can take place
20/40/64 CPU	on COM1 as long as no more than three SRTP channels are active.
	If the application program includes any transition coils within a jump
Positive or negative	region, sub-block, or similar area, and the transition coils are not
transition coils in a	executed in the scans after the coils set their reference address, and the
jump region or a sub-	PLC is placed in Stop No I/O mode, the coils' reference will not respond
block	as expected after a Clear Initial Values command. In such a situation, use
	the Clear All command instead.
Output Waveform	During DMM fragging the read of the Company of the Company of the
During PWM	During PWM frequency change, a Long OFF or Long ON may occur at the
Frequency Change	first pulse of the new frequency. Examples are illustrated below.
	Frequency change initiated
	↓
Long OFF	
	Long OFF
	If the transition occurs while the pulse is OFF, the maximum time the
	output is OFF is equal to the old frequency OFF state duration plus the
	new frequency pulse duration.

Issue	Description
Long ON Compatibility with old Machine Edition (below version 6.0), and new firmware (3.83 or later) for Ethernet throughput	If the transition occurs while the pulse is ON, the maximum time for which the output is ON is equal to the old frequency ON state duration plus the new frequency pulse duration. (Micro 20/40/64). Old Machine Edition allows data rates 300 through 38400 bps for the Ethernet option module on Port2. But the new firmware supports only 19200, 38400, and 175000 bps. When any data rate lower than 19200 bps is configured, communication with the Ethernet Option module is not guaranteed.
enhancement The RS-232/USB module is auto- configured as an Ethernet Option module with SNP protocol and 175Kbps data rate.	Auto-configuration with these option modules connected will result in Port 2 being configured to 175000bps, which is not supported for these modules. To configure Port 2 to a valid data rate connect to Port 1 and download a configuration with a valid data rate for Port 2.
Watchdog timeout during SVC_REQ 52:	When this service request is used to read the reference memory from the flash when the size of the stored data is more than 35Kbytes, the watchdog expires. This happens because a stored block that exceeds 35K requires more than 200ms to search, which causes the watchdog timer to expire. It is recommended that you limit the usage of user flash to a maximum of 35KB to avoid the watchdog expiration. This restriction applies to Micro 14, Micro 23, Micro 28, and Micro 28 ESCP models, which have 64KB of logic-driven user flash.
Nano and Analog Nano models go to blink mode when a firmware update is aborted:	If the update is aborted (due to power failure or unplugging of serial communication cable) after downloading 2-3 blocks, the unit goes into blink mode after power cycling. (The OK LED blinks continuously.) To return the PLC to normal operation, power cycle the unit with the external RUN/STOP switch in RUN mode.
Serial to Ethernet Adapter Module IC200SET001: the data rate for serial I/O	If two VersaMax Micro CPUs will exchange data via Serial to Ethernet Adapter Modules (IC200SET001), and they will use Serial I/O Protocol, the data rate for the modules must be less than 38400 bps. For a Micro-20/40/64 CPU, this problem can be avoided by using an Ethernet Option Module instead of the Serial to Ethernet Adapter module.

Installation in Hazardous Locations

WARNING

Explosion Hazard

Substitution of any components may impair suitability for Class I, Division 2.

WARNING

Explosion Hazard

Do not replace modules unless power has been switched off or the area is known to be non-hazardous.

A WARNING

Explosion Hazard

Do not connect or disconnect equipment unless power has been switched off or the area is known to be non-hazardous.

A WARNING

Fire, Explosion, and Severe Burn Hazard.

Do not recharge, disassemble, heat above 212°f (100°c), incinerate, or expose contents to water.

Recommendation: If PLC is used in a chemical environment, it could affect and degrade relay devices. It is recommended to inspect the sealed relay device periodically and check for any degradation of the materials and to replace the complete product, not the sealed device if any degradation is found.

Installation sur des sites á risque

A ATTENTION

Danger d'explosion

La substitution de quelque composant que ce soit peut diminuer la pertinence pour la Classe I, Division 2.

A ATTENTION

Danger d'explosion

Ne pas remplacer les modules sauf si l'alimentation est deconnectee ou que le lieu est est connu comme etant sans danger.

A ATTENTION

Danger d'explosion

Ne pas connecter ni deconnecter de cables sauf si l'alimentation est deconnectee ou que le lieu est connu comme etant sans danger.

A ATTENTION

Danger d'incendie, explosion et severes brulures.

ne pas recharger, demonter, chauffer au dessus de 212°f (100°c), incinerer, ou exposer le contenu a l'eau.

Recommendation: Si l'automate est utilise dans un environemment chimique, il peut affecter le comportement des relais voir les degrader. Il est recommende d'inspecter les relais periodiquement et verifier la non degradation de ceuxci et de remplacer le produit complet en cas de deterioration.

Backup Batteries

Two types of backup batteries are available, IC200ACC403 (coin type, 3.0 VDC @210MAH) and IC200ACC414 (cylinder type, 3.6 VDC @790MAH).

Batteries de rechange

Two types de batteries de rechange sont disponibles (type pièce, 3.0 VDC @210MAH) et IC200ACC414 (type cylindre, 3.6 VDC @790MAH).

General Contact Information

Home link: http://www.emerson.com/industrial-automation-controls

Knowledge Base: https://www.emerson.com/industrial-automation-controls/support

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Note: If the product is purchased through an Authorized Channel Partner, please contact the seller directly for any support.

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